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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PAUL A. FARRAR Examiner: Michail Kornakov

Serial No.: 10/681,481 Group Art Unit: 1746

Filed: October 8, 2003 Docket: 1303.112US1

Title: METHOD OF CLEANING SEMICONDUCTOR SURFACES

RESPONSE TO RESTRICTION REQUIREMENT

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

In response to the Restriction Requirement mailed October 10, 2006, Applicant submits the following.